

FIG. 1

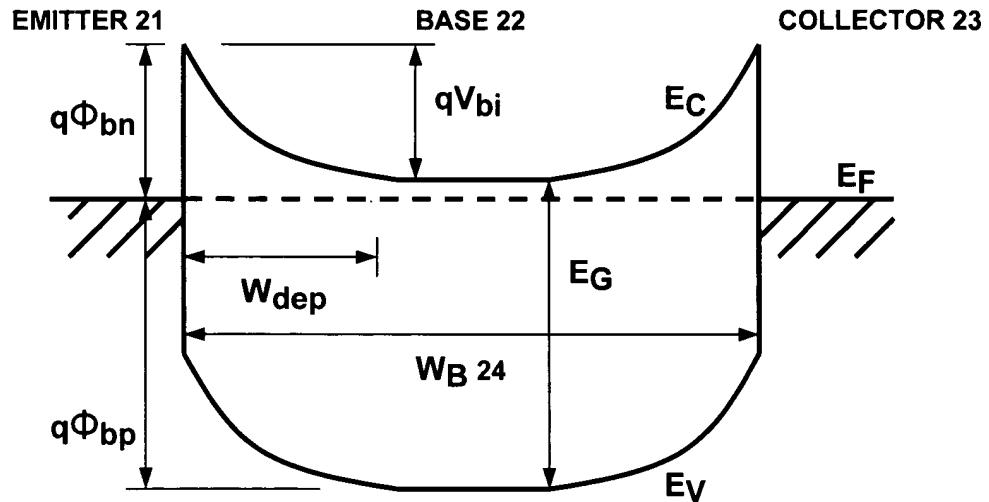


FIG. 2a

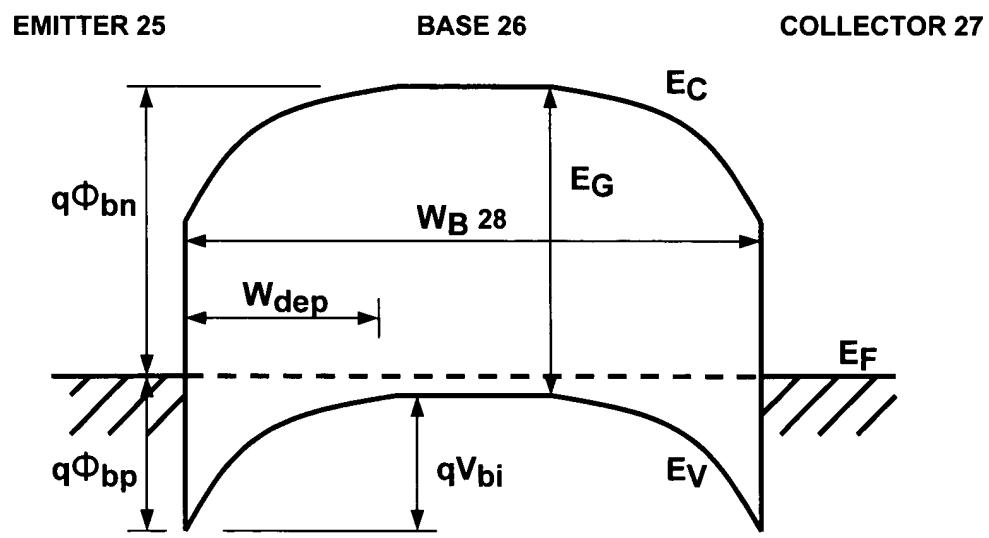


FIG. 2b

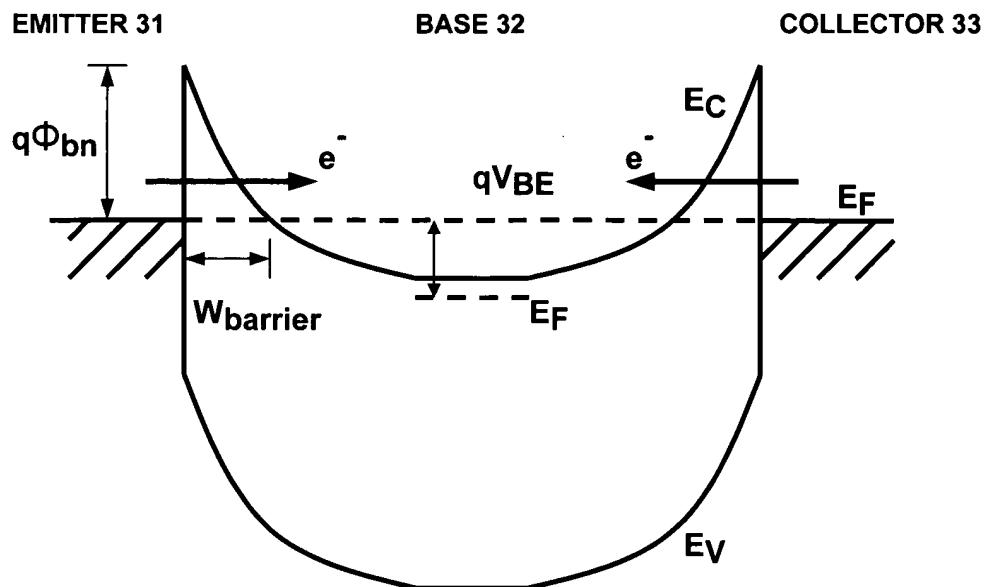


FIG. 3a

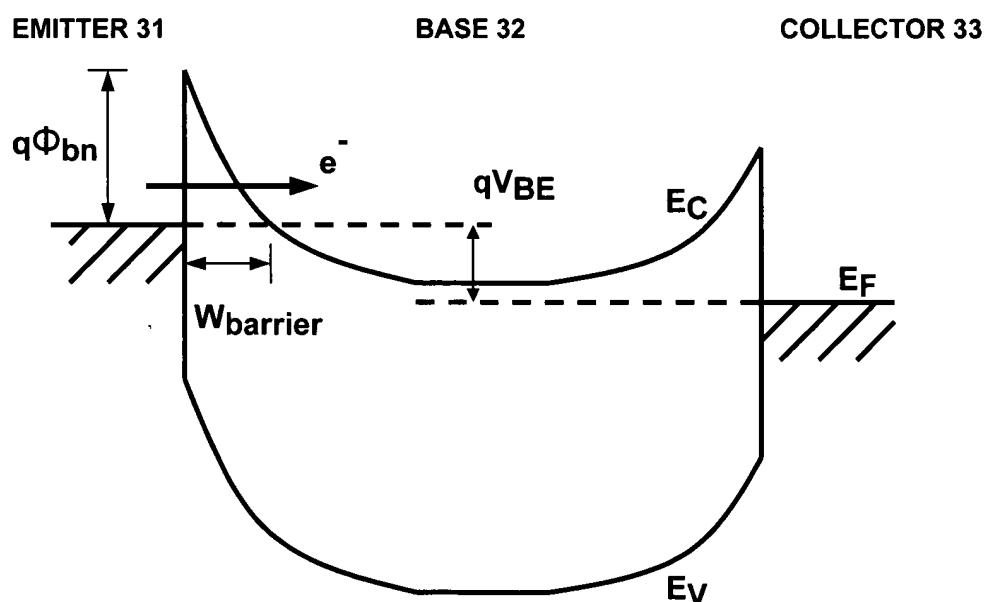


FIG. 3b

EMITTER 41 BASE 42 COLLECTOR 43

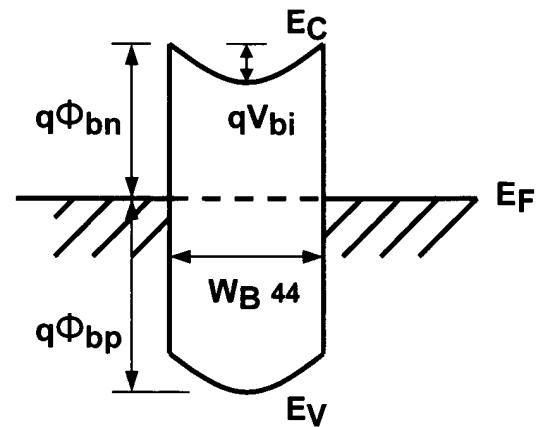


FIG. 4a

EMITTER 45 BASE 46 COLLECTOR 47

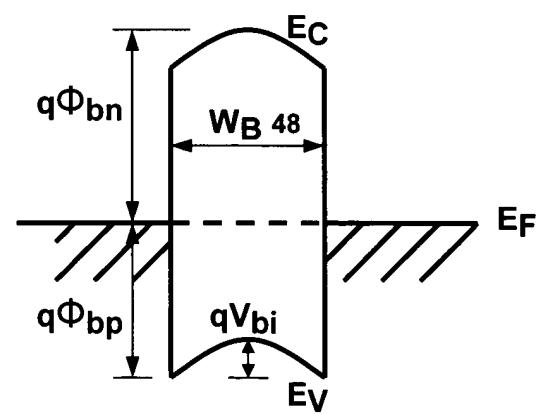


FIG. 4b

EMITTER 51 BASE 52 COLLECTOR 53 Emitter 51 Base 52 Collector 53

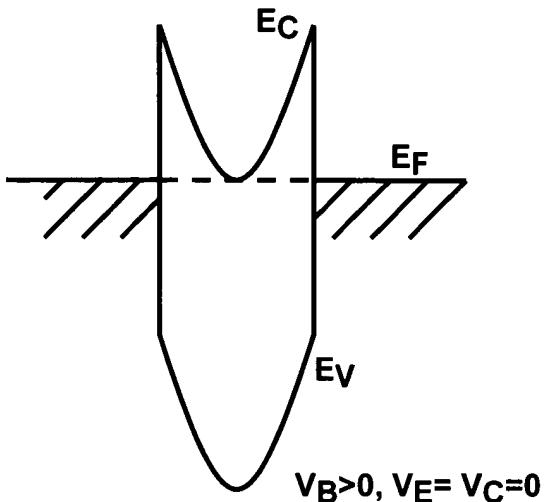
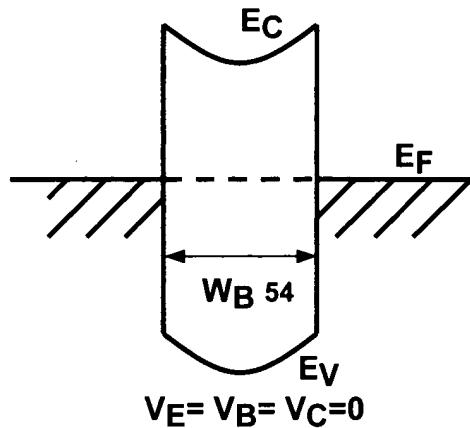


FIG. 5a

FIG. 5b

EMITTER 51 BASE 52 COLLECTOR 53 Emitter 51 Base 52 Collector 53

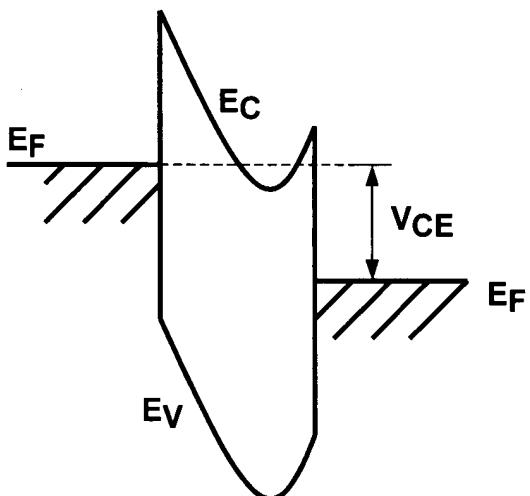
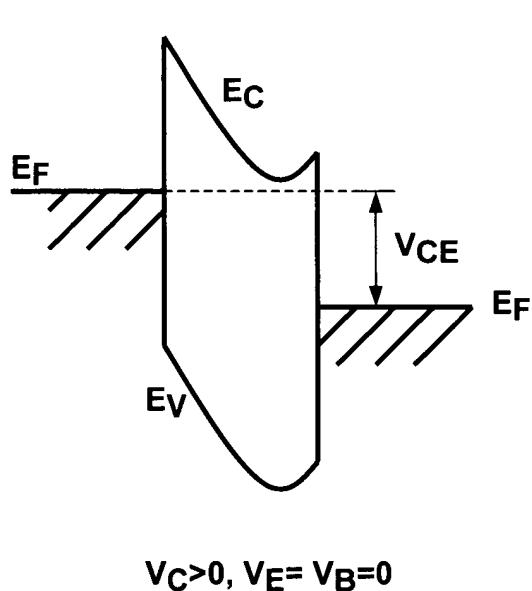


FIG. 5c

FIG. 5d

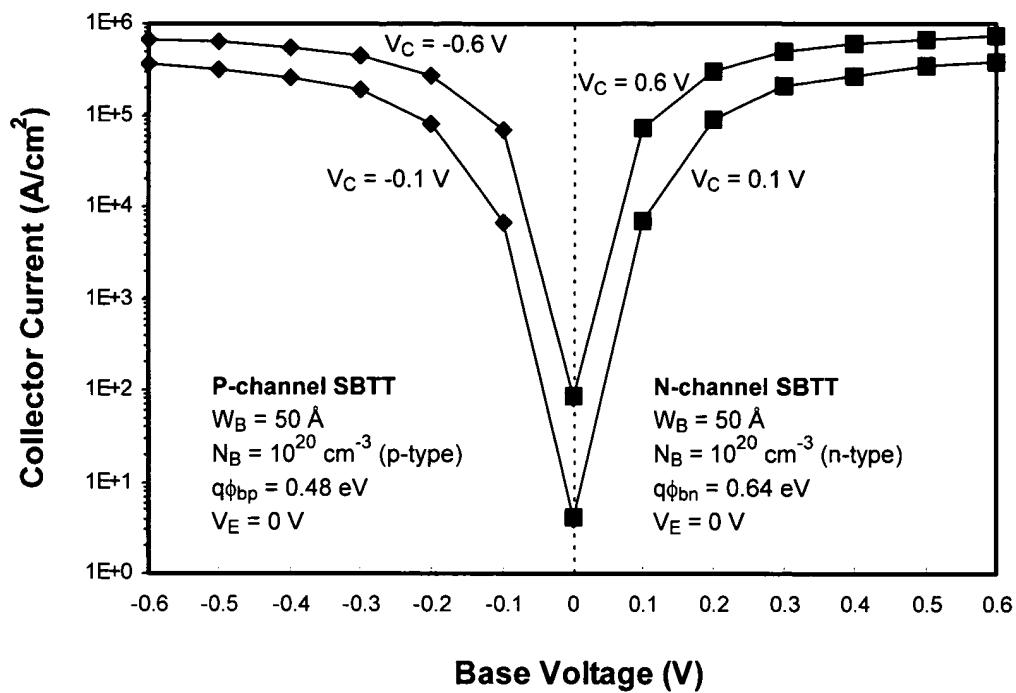


FIG. 6

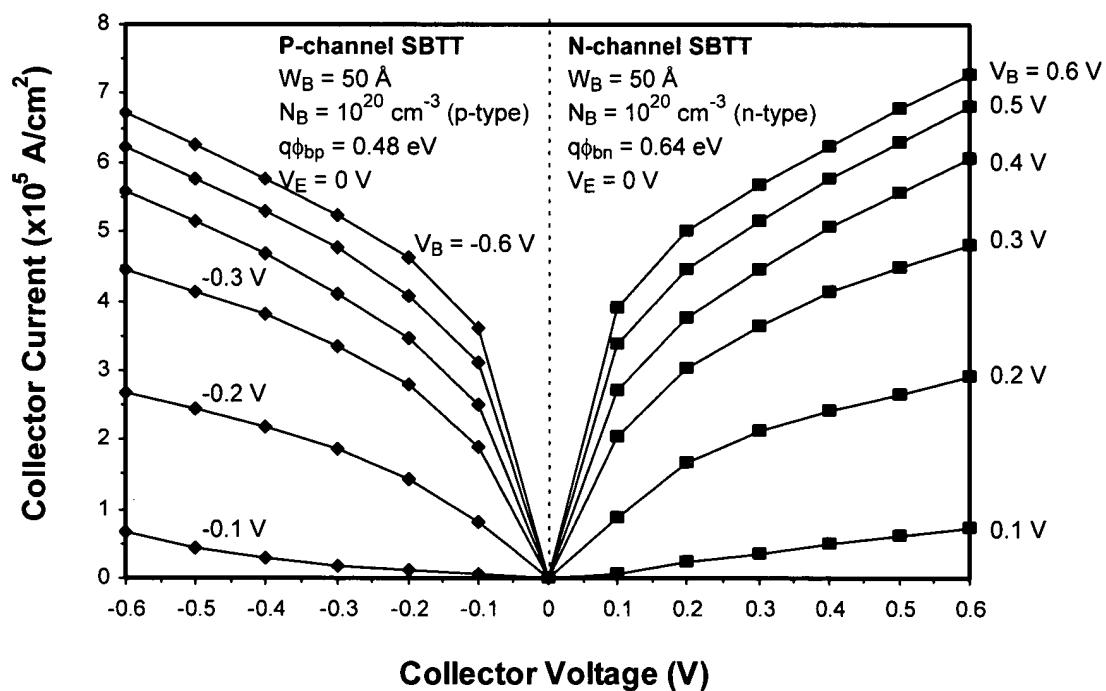


FIG. 7

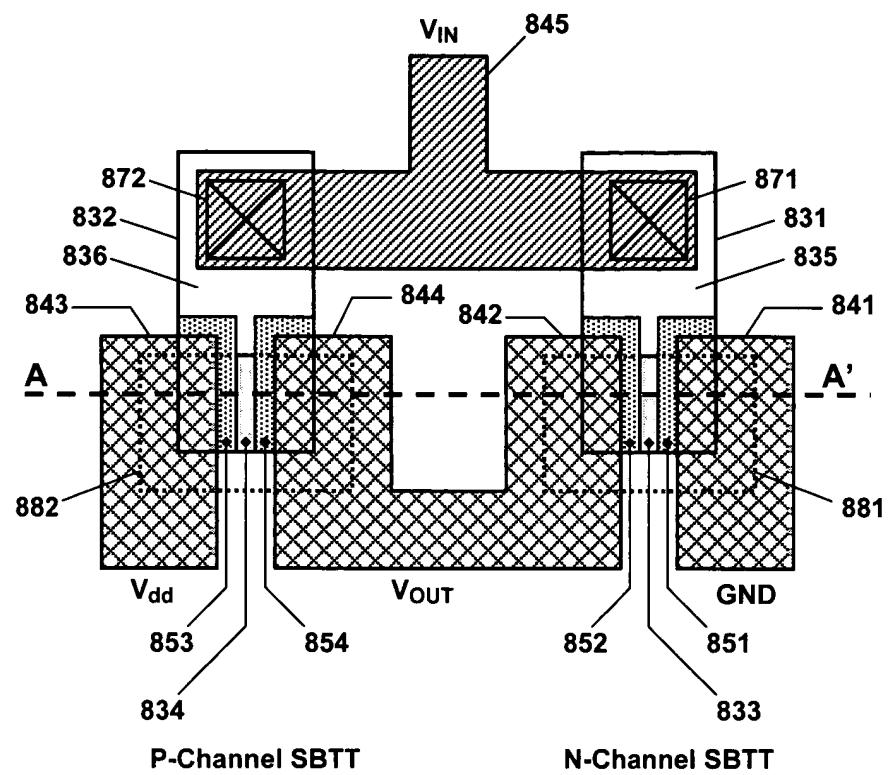


FIG. 8

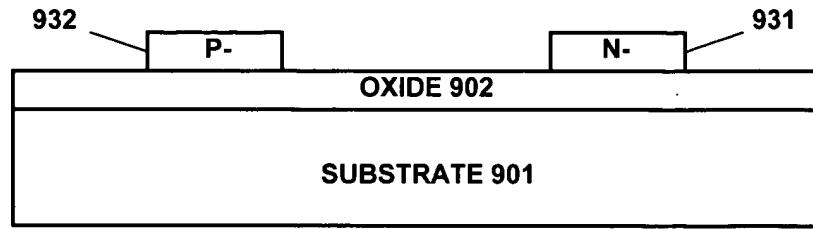


FIG. 9a

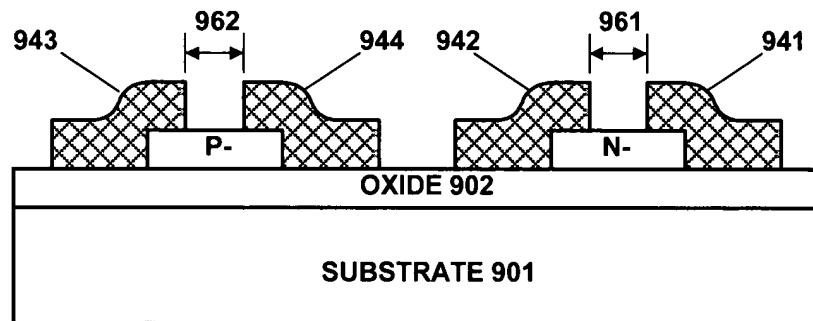


FIG. 9b

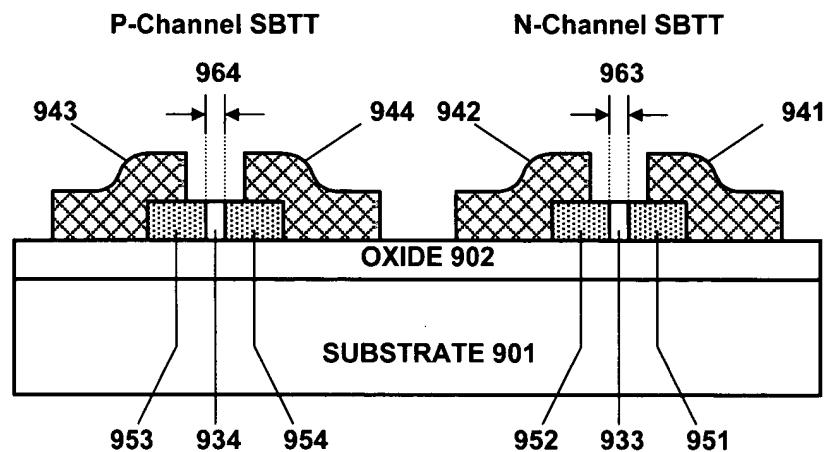


FIG. 9c

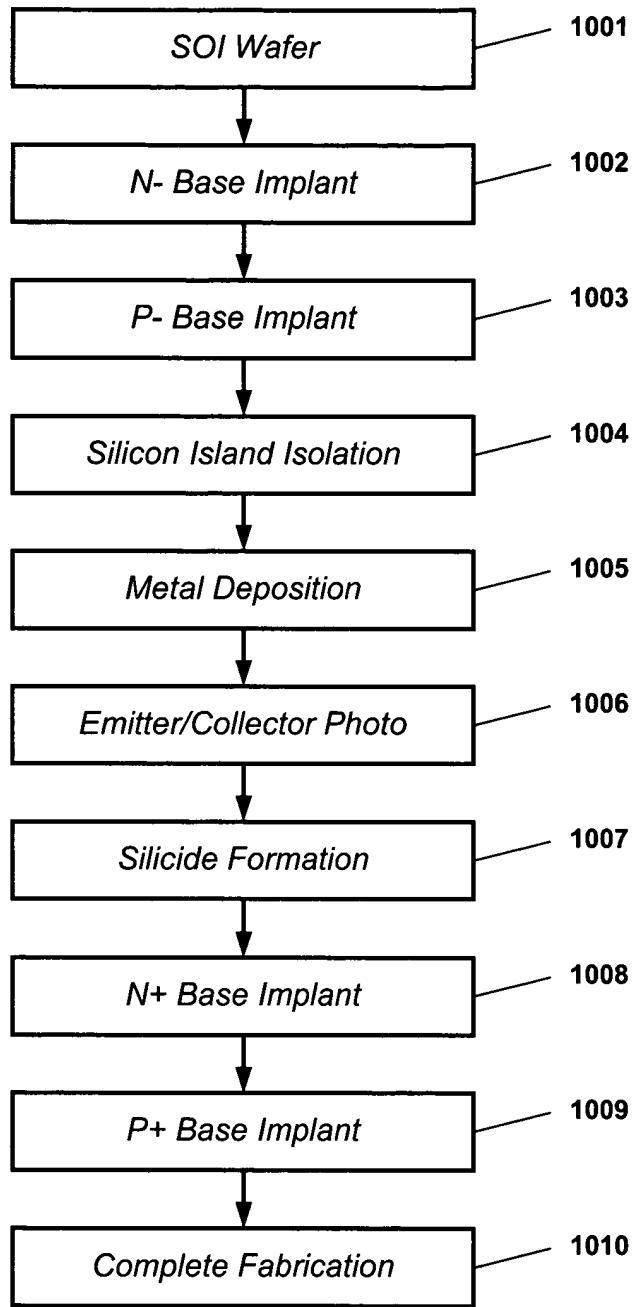


FIG. 10

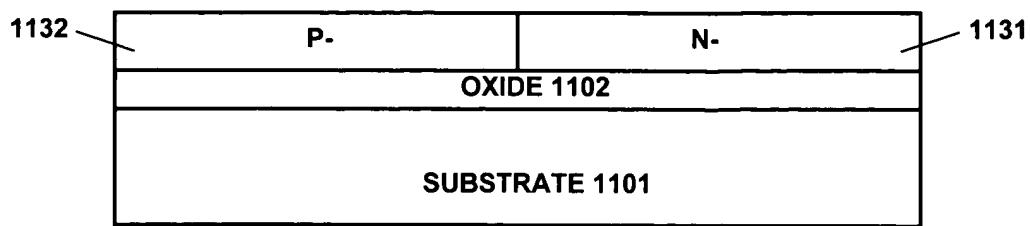


FIG. 11a

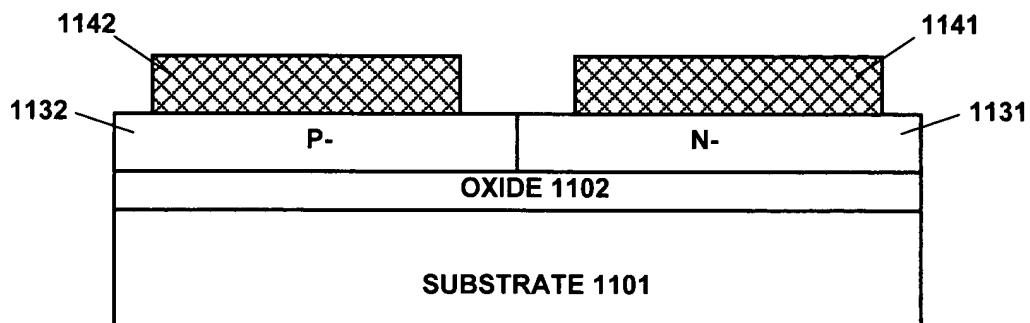
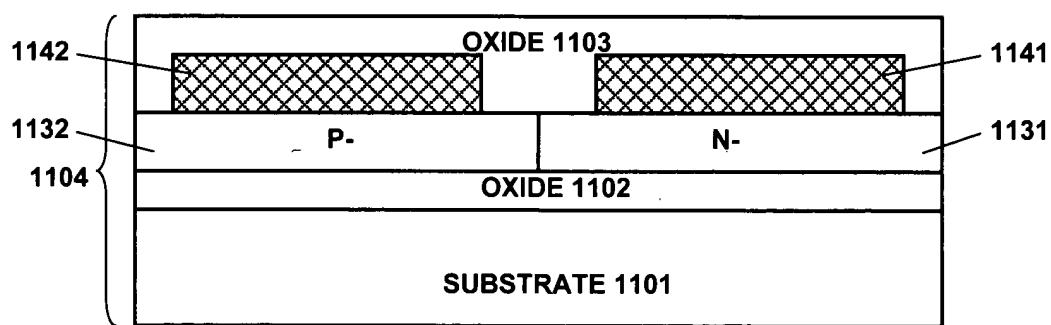
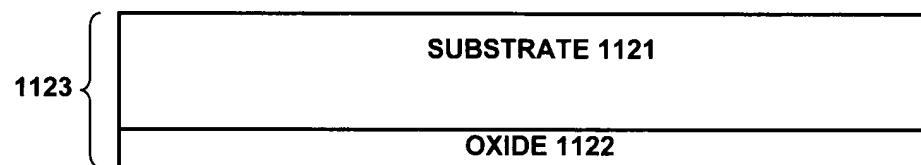


FIG. 11b



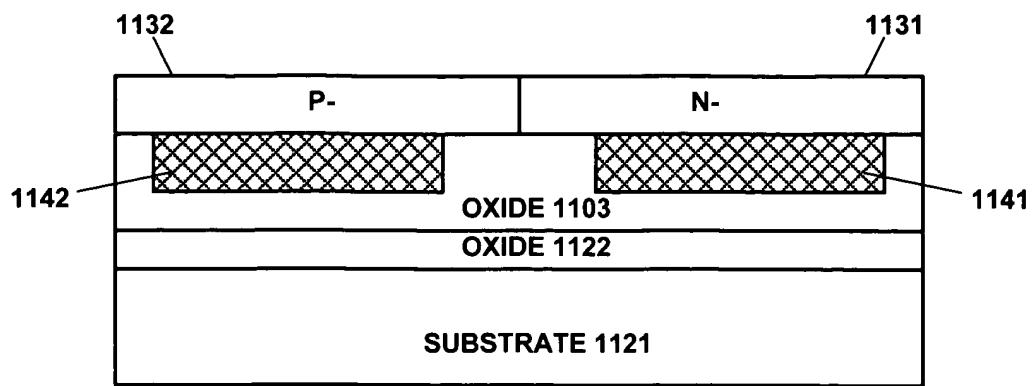


FIG. 11d

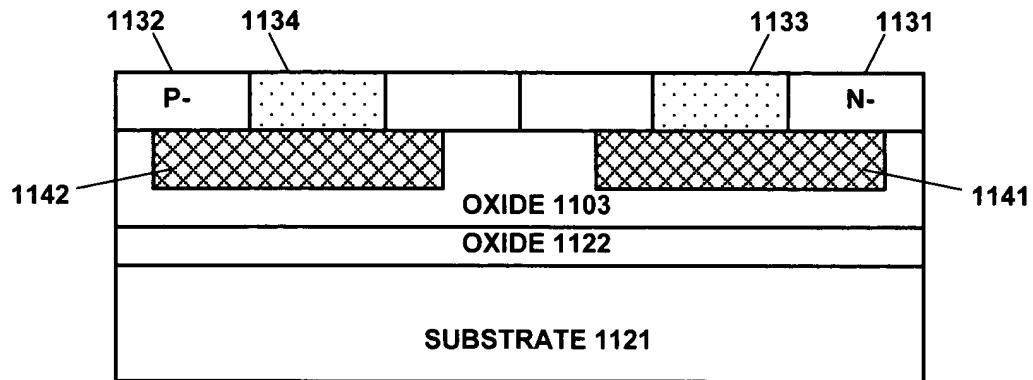


FIG. 11e

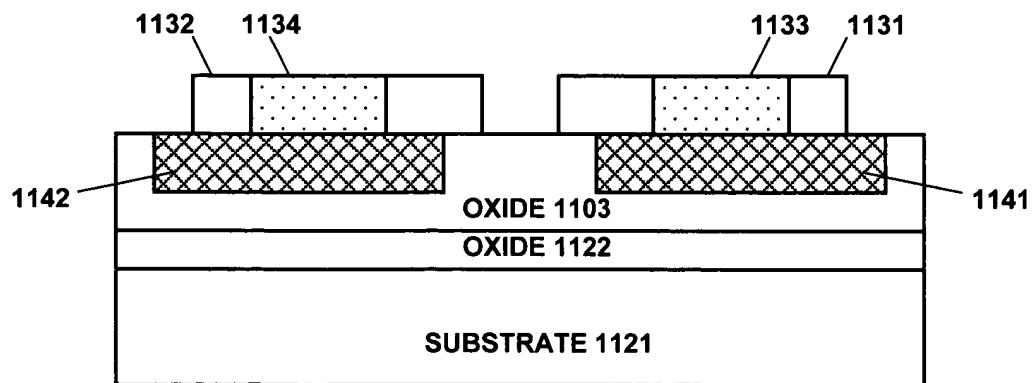


FIG. 11f

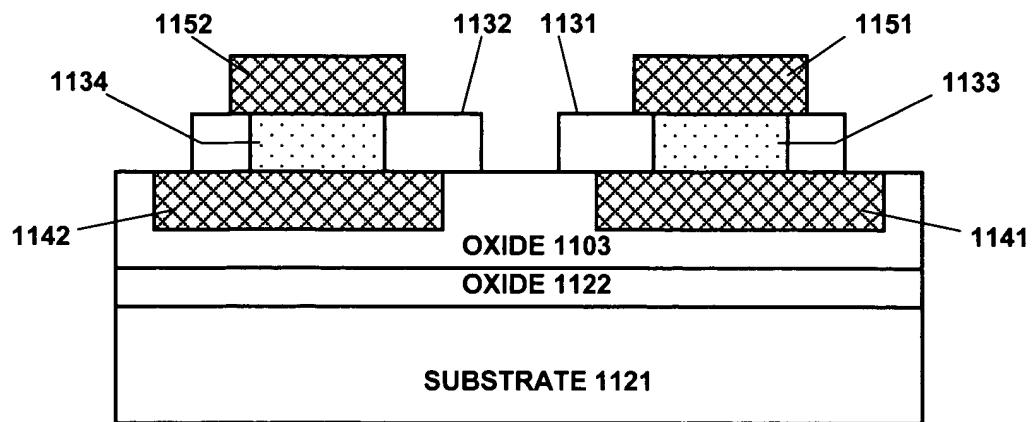


FIG. 11g

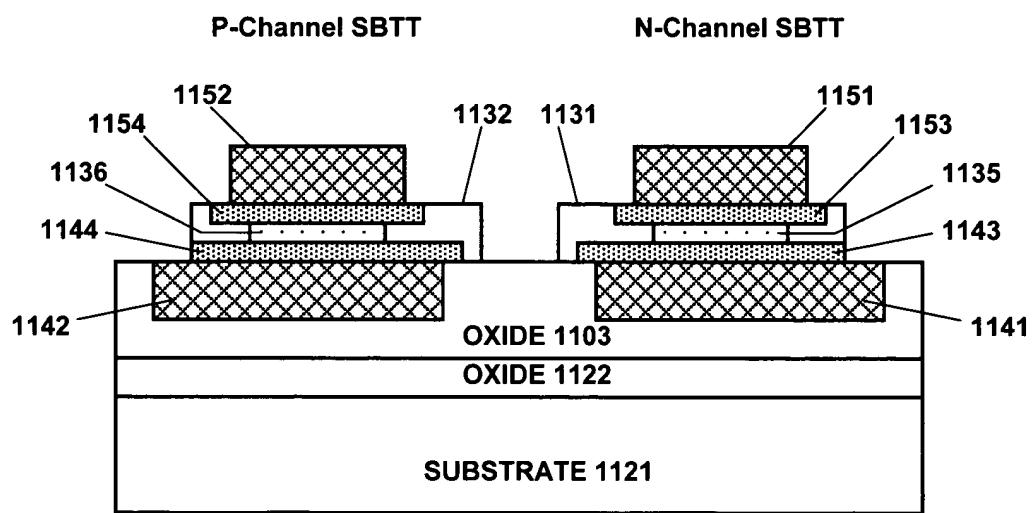


FIG. 11h

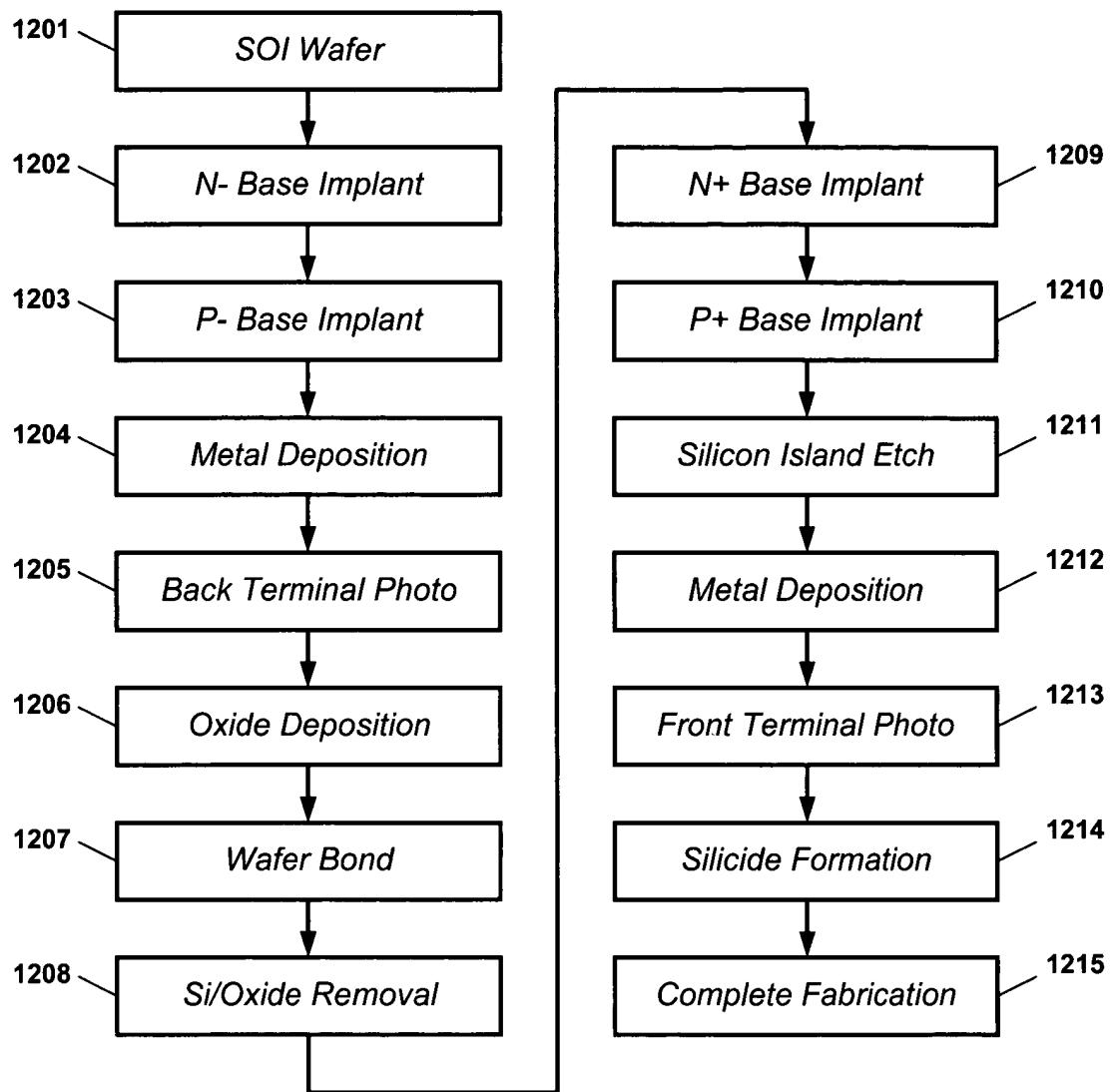


FIG. 12